	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

JC 2
JC 856

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED _AC CS_ Q.A. _CS_	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/657833		375 381	111	2631 2644	M. HARVEY Faulks

APPLICANTS

Timothy Shuttleworth

TITLE

System and method for synchronizing the clock frequencies of power processing devices and digital signal processing devices in an electronic system

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

☐ **TERMINAL DISCLAIMER**	**DRAWINGS**			**CLAIMS ALLOWED**	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)		(Date)	**NOTICE OF ALLOWANCE MAILED**	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Primary Examiner)		(Date)	**ISSUE FEE**	
				Amount Due	Date Paid
☐ The terminal ____ months of this patent have been disclaimed.	(Legal Instruments Examiner)		(Date)	**ISSUE BATCH NUMBER**	

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